



**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Application No.: 10/008,643

Filed: November 9, 2001

Inventor(s):

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Carl Cavanaugh, and James P.  
Freyensee

Title: Synchronization of  
Distributed Simulation  
Nodes by Keeping Timestep  
Schedulers in Lockstep

§ Examiner: Lim, Krisna  
§ Group/Art Unit: 2153  
§ Atty. Dkt. No: 5181-98000

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below.

Lawrence J. Merkel

**Printed Name**

3/21/05  
Date \_\_\_\_\_

Signature

Date

**RESPONSE TO OFFICE ACTION OF  
February 18, 2005**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of February 18, 2005, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.